

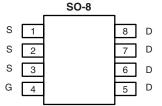
**Vishay Siliconix** 

# N-Channel 30-V (D-S) MOSFET with Schottky Diode

MOSFET PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<sub>S</sub> (V) R <sub>DS(on)</sub> (Ω)				
30	0.016 at V <sub>GS</sub> = 10 V	9.5			
	0.021 at V <sub>GS</sub> = 4.5 V	7.7			

### SCHOTTKY PRODUCT SUMMARY

V <sub>DS</sub> (V)	V <sub>SD</sub> (V) Diode Forward Voltage	I <sub>F</sub> (A)
30	0.50 V at 1.0 A	1.4



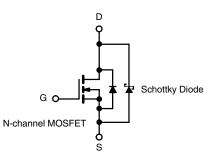
Top View

Ordering Information: Si4812BDY-T1-E3 (Lead (Pb)-free) Si4812BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Available
- LITTLE FOOT<sup>®</sup> Plus Power MOSFET ٠
- 100 % R<sub>g</sub> Tested ٠





ABSOLUTE MAXIMUM RATINGS	T <sub>A</sub> = 25 °C, unle	ss otherwise	noted		
			Limit		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage (MOSFET)		V <sub>DS</sub>	30		V
Reverse Voltage (Schottky)		VDS −			
Gate-Source Voltage (MOSFET)		V <sub>GS</sub>	± 20		
Continuous Drain Current (T <sub>J</sub> = 150 °C) (MOSFET	<sub>г\а, b</sub> Т <sub>A</sub> = 25 °С	I <sub>D</sub>	9.5	7.3	
Continuous Drain Current $(T_J = 150^{\circ}C)$ (MOSFE)	T <sub>A</sub> = 70 °C	U	7.7	5.9	I
Pulsed Drain Current (MOSFET)		I <sub>DM</sub>	50		
Continuous Source Current (MOSFET Diode Conduction) <sup>a, b</sup>		۱ <sub>S</sub>	2.1	1.2	А
Average Forward Current (Schottky)		١ <sub>F</sub>	1.4	0.8	
Pulsed Forward Current (Schottky)		I <sub>FM</sub>	30		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	5 1.25		1
Avalanche Energy		E <sub>AS</sub>			mJ
Maximum Power Dissipation (MOSFET) <sup>a, b</sup>	T <sub>A</sub> = 25 °C		2.5	1.4	w
	T <sub>A</sub> = 70 °C	PD	1.6	0.9	
Maximum Power Dissipation (Schottky) <sup>a, b</sup>	T <sub>A</sub> = 25 °C	· U	2.0	1.2	
waximum Power Dissipation (Schottky) <sup>4, 2</sup>	T <sub>A</sub> = 70 °C	Γ	1.3	0.8	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55	5 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter	Device	Symbol	Typical	Maximum	Unit
	MOSFET		40	50	°C/W
Maximum Junction-to-Ambient (t $\leq$ 10 s) <sup>a</sup>	Schottky	<b>B</b>	50	60	
	MOSFET	R <sub>thJA</sub>	72	90	
Maximum Junction-to-Ambient (t = Steady State) <sup>a</sup>	Schottky		85	100	
	MOSFET	P	18	23	
Maximum Junction-to-Foot (t = Steady State) <sup>a</sup>	Schottky	R <sub>thJF</sub>	24	30	

Notes:

a. Surface Mounted on FR4 board. b.  $t \le 10$  s.

Document Number: 73038 S-83039-Rev. D, 29-Dec-08

# Si4812BDY

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<b>MOSFET AND SCHOTTKY SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted								
arameter Symbol		Test Conditions	Min.	Тур.	Max.	Unit		
Static	1					1		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1		3	V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA		
Zero Gate Voltage Drain Current (MOSFET and Schottky)		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		0.004	0.100			
	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 100 ^{\circ}\text{C}$ 0.7			10	mA		
		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$		3.0	20	1		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α		
Drain-Source On-State Resistance <sup>a</sup>	Б	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 9.5 \text{ A}$	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 9.5 \text{ A}$ (		0.016	0		
	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 7.7 \text{ A}$		0.0165	0.021	Ω		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 9.5 \text{ A}$		45		S		
Schottky Diode Forward Voltage <sup>a</sup>	V	I <sub>S</sub> = 1.0 A, V <sub>GS</sub> = 0 V	0.45 0.5		0.50	N/		
	V <sub>SD</sub>	$I_{S}$ = 1.0 A, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C		0.33	0.42	V		
Dynamic <sup>b</sup>			-					
Total Gate Charge	Qg			8.5	13			
Gate-Source Charge	Q <sub>gs</sub>	$Q_{gs}$ V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 5 V, I <sub>D</sub> = 9.5 A		3		nC		
Gate-Drain Charge	Q <sub>gd</sub>			2.6		1		
Gate Resistance	Rg		0.3	0.7	1.1	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>			15	25			
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		13	20			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_{D}\cong$ 1 A, $V_{GEN}$ = 10 V, $R_{g}$ = 6 $\Omega$		20	30	ns		
Fall Time	t <sub>f</sub>			8	15			
Source-Drain Reverse Recovery Time		I <sub>F</sub> = 1.0 A, dl/dt = 100 A/μs		22	35			

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

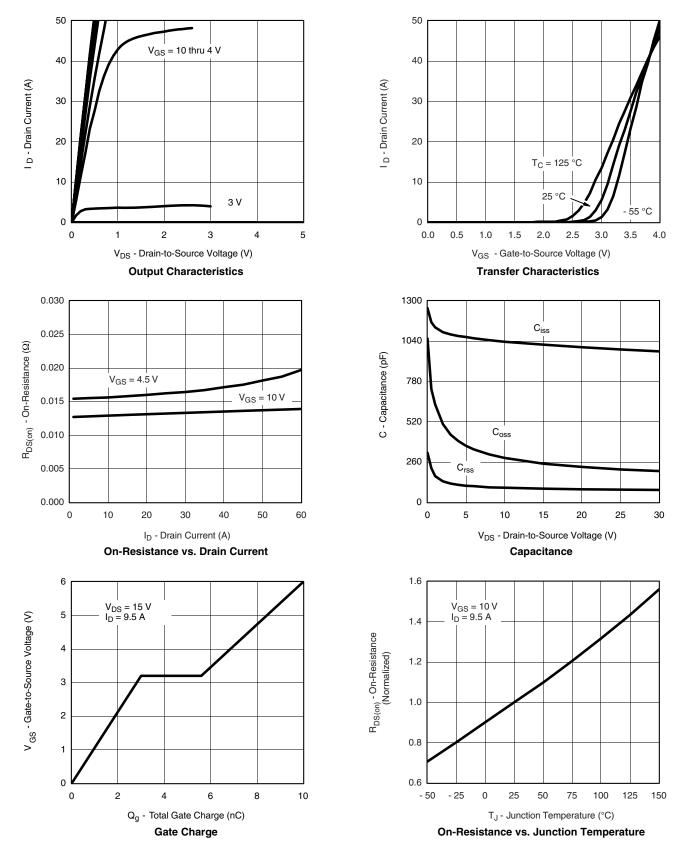




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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

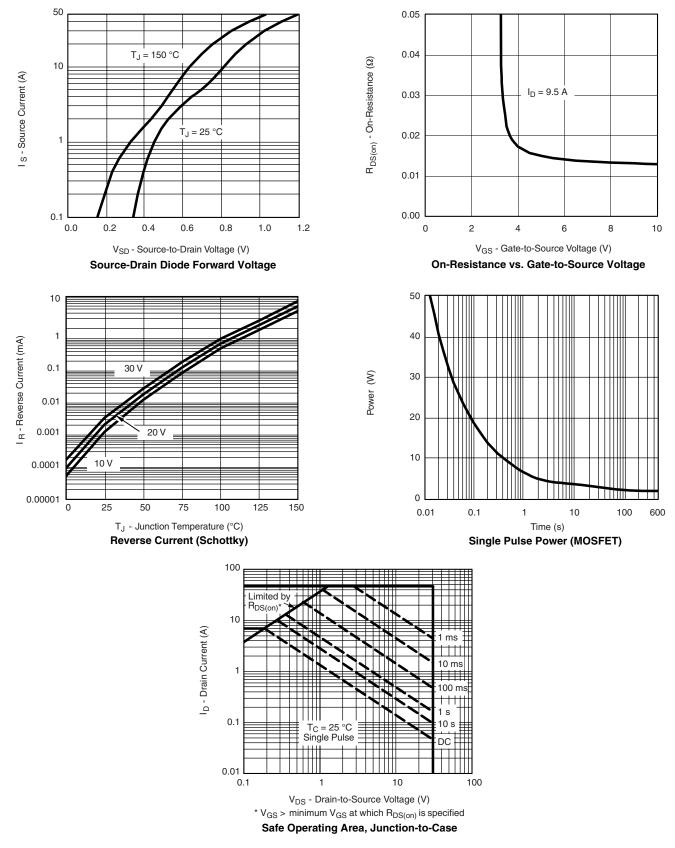


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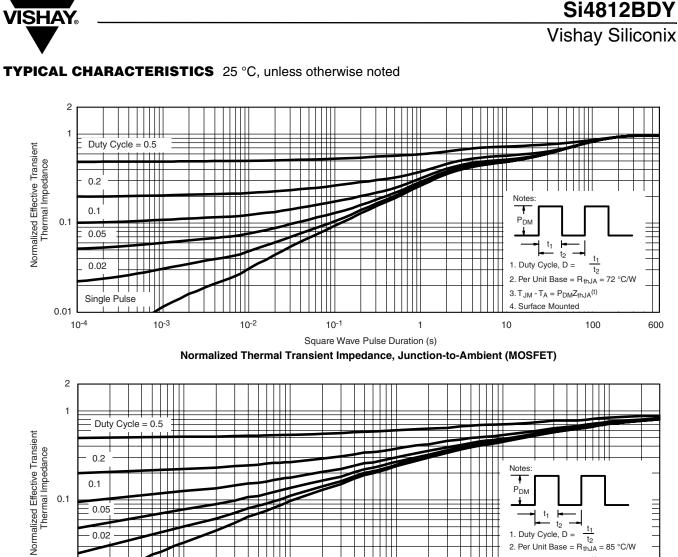
### Si4812BDY

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VISHA



Single Pulse

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10<sup>-3</sup>

Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Ambient (Schottky)

10-1

10-2

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0.01

10-4

2. Per Unit Base = R<sub>thJA</sub> = 85 °C/W 3.  $T_{JM}$  -  $T_A = P_{DM}Z_{thJA}^{(t)}$ 

10

30

4. Surface Mounted

1



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